

Figure 1

# Design System

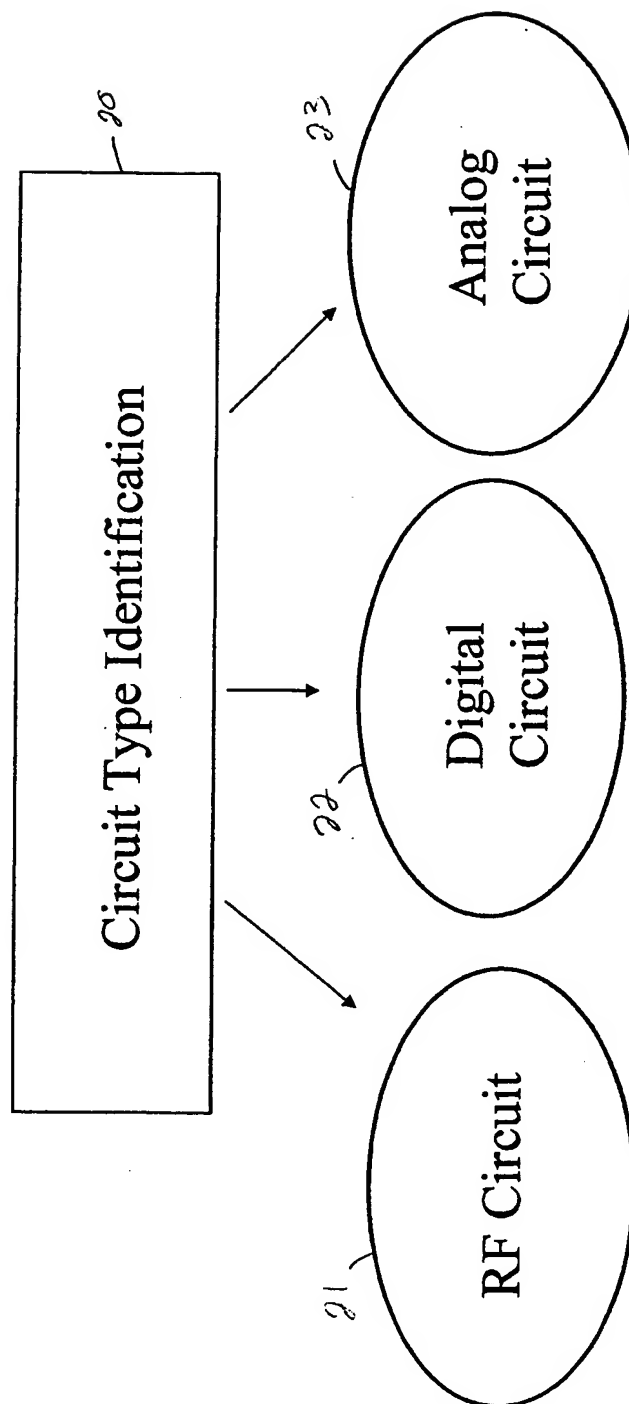


Figure 2

# Design System

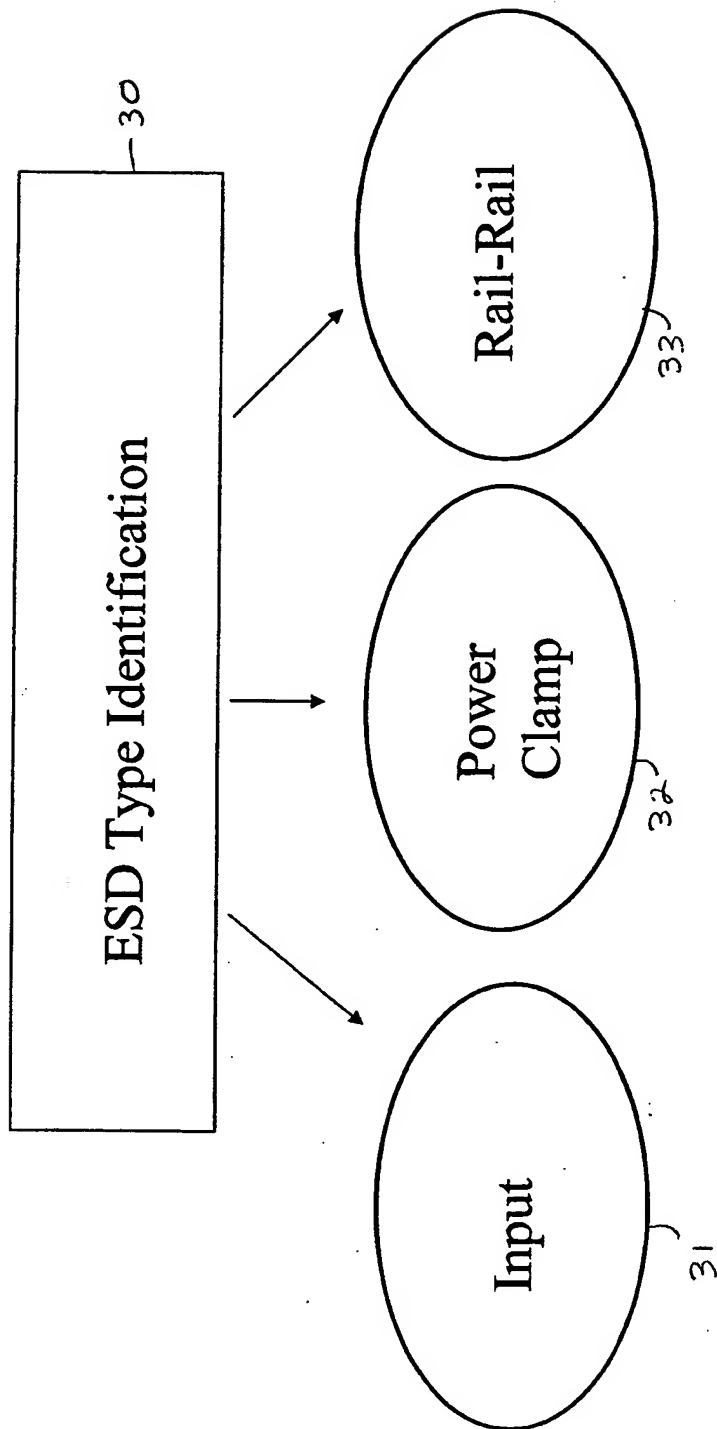


Figure 3

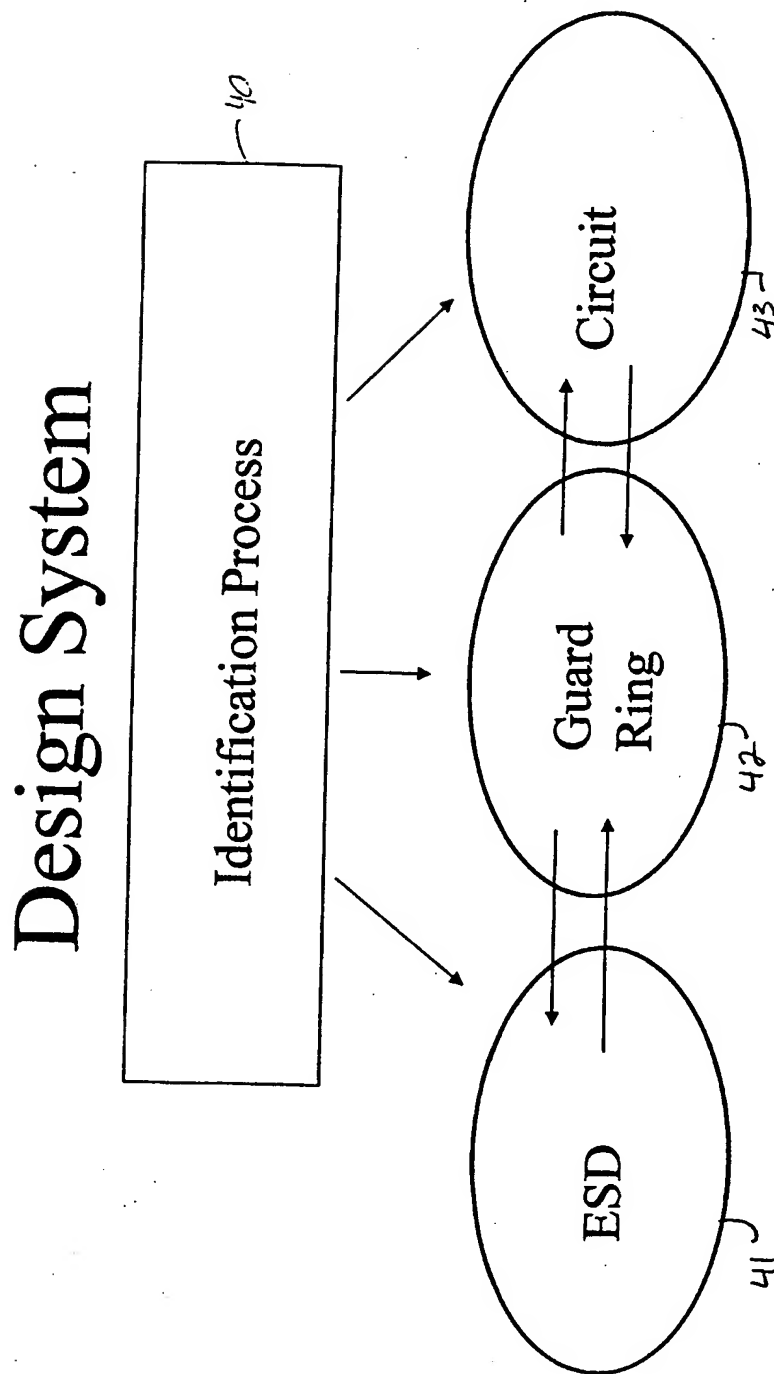


Figure 4

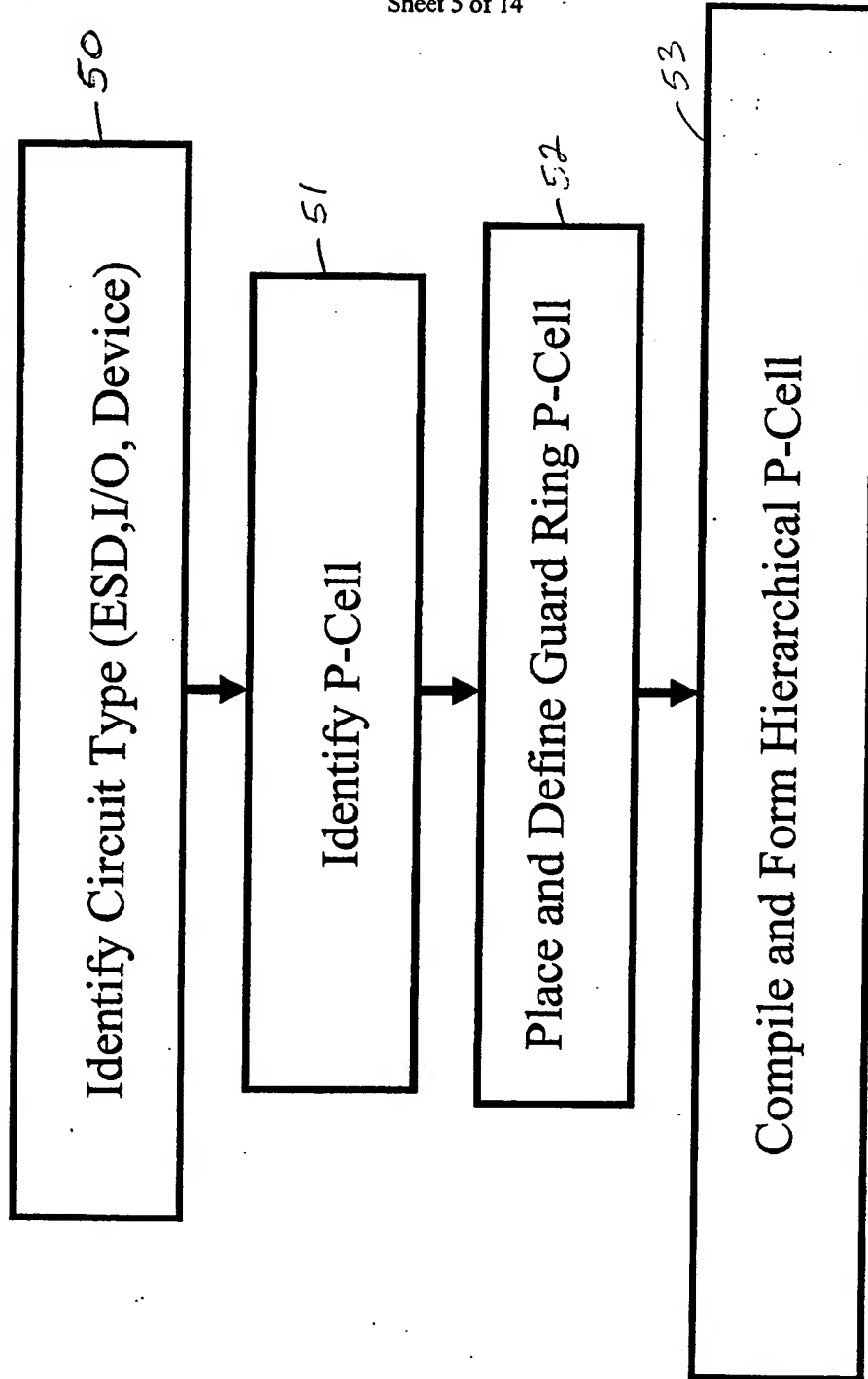
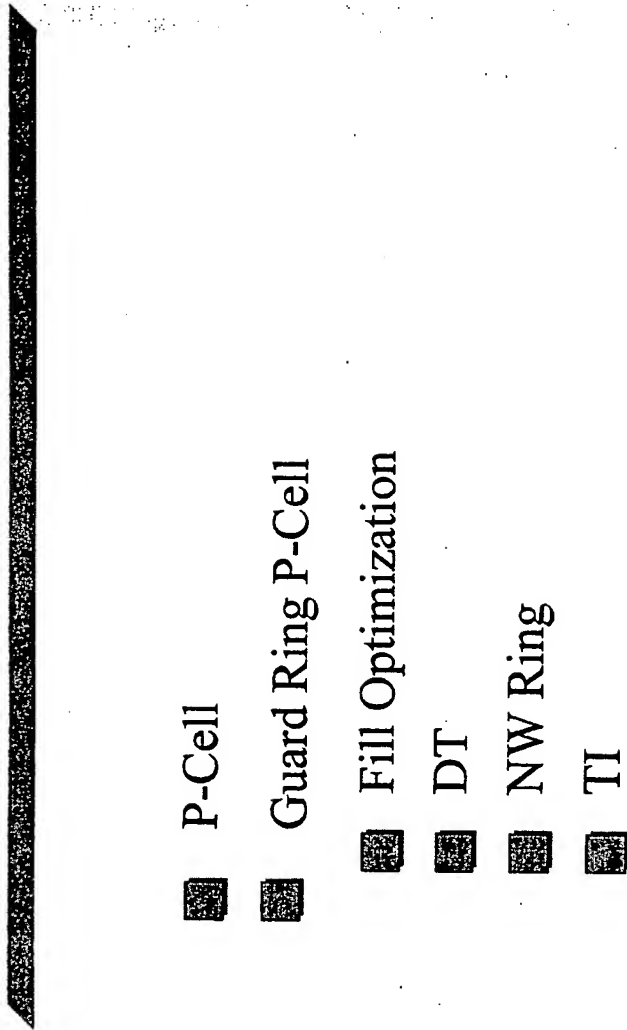
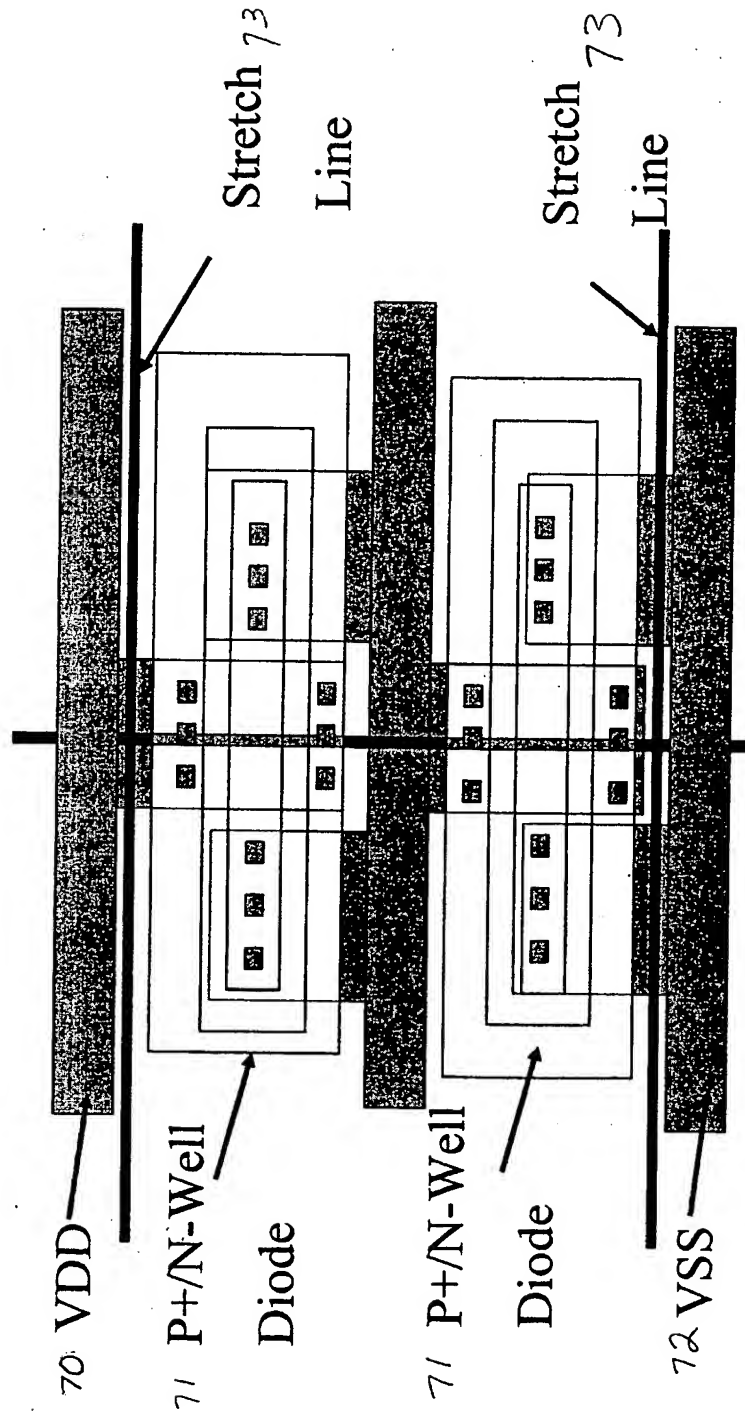


Figure 5



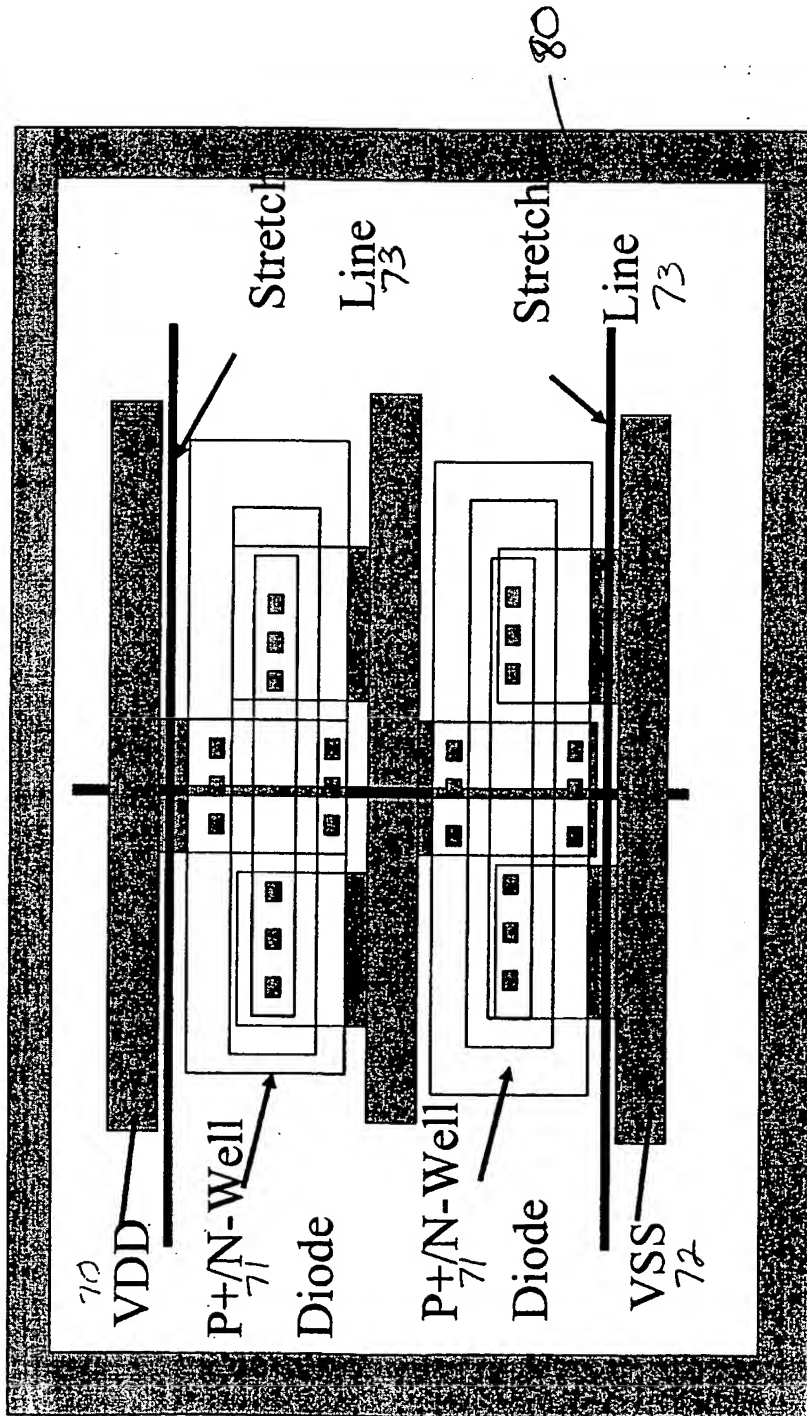
P-Cell / Guard Ring Design P-Cell Graphical Unit Interface

Figure 6



Graphical ESD HIERARCHICAL PCELL

Figure 7



ESD HIERARCHICAL PCELL w/ GUARD RING

Figure 8



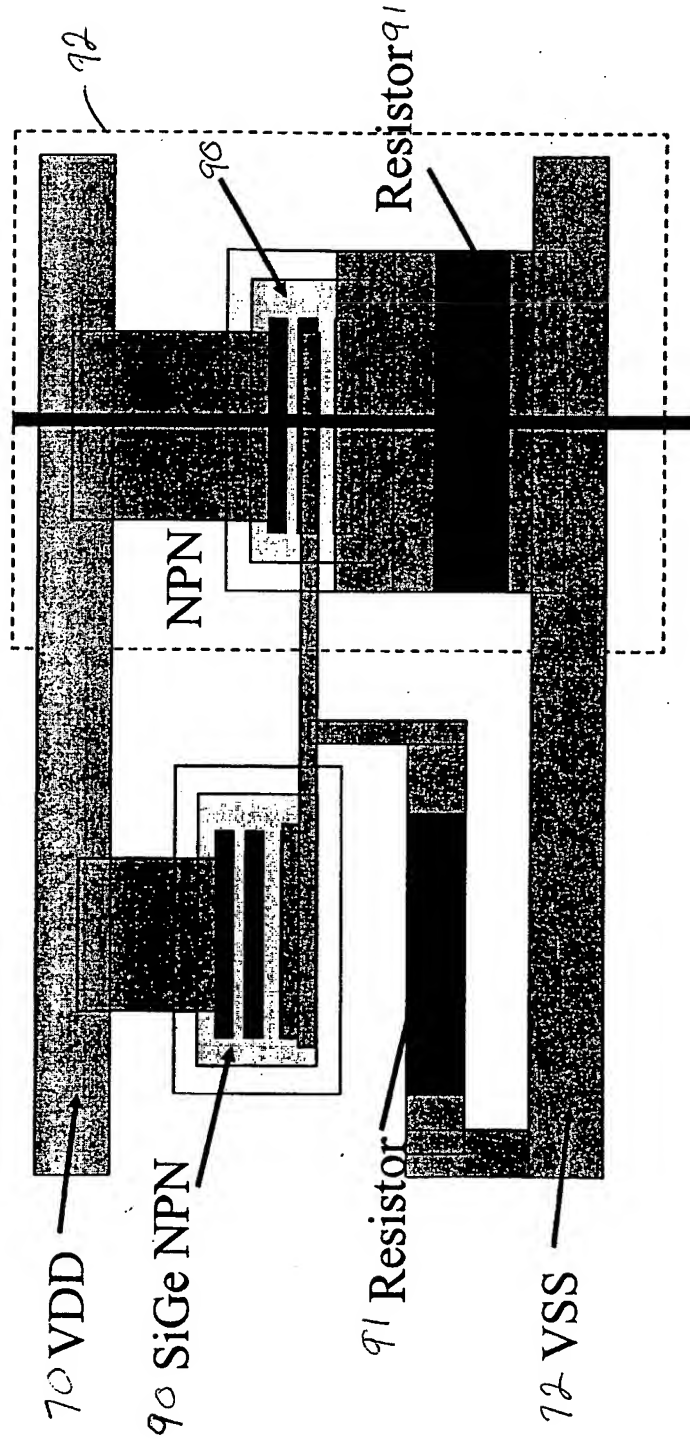


Figure 9

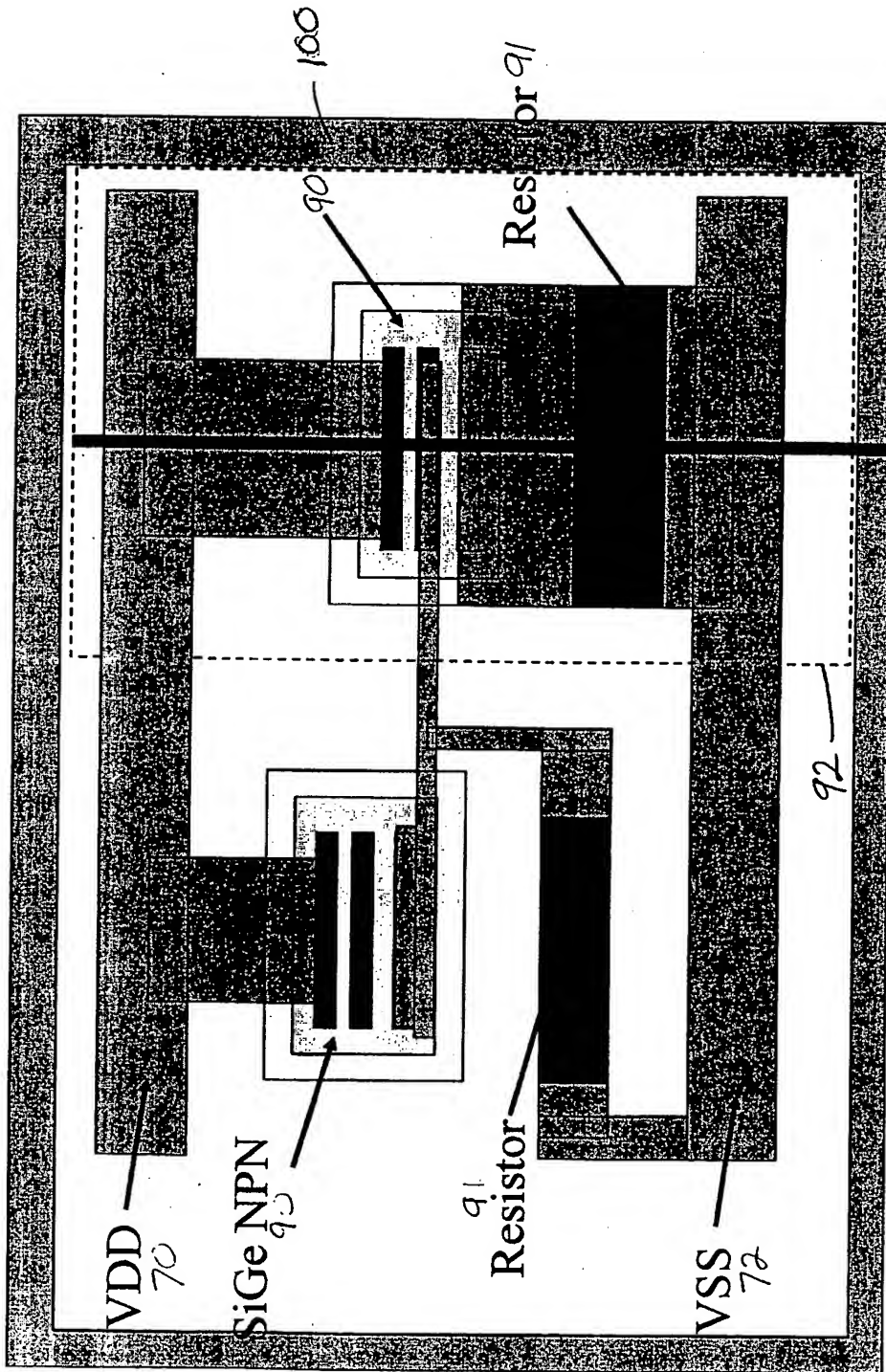


Figure 10

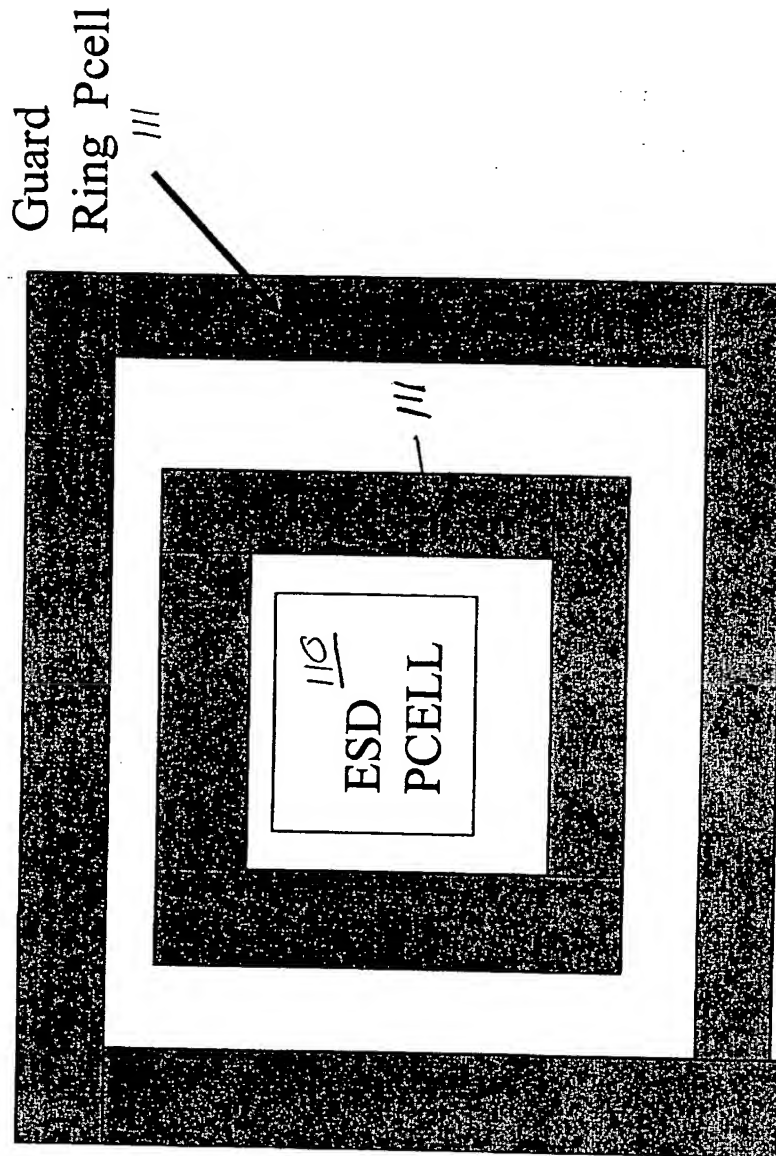


Figure 11

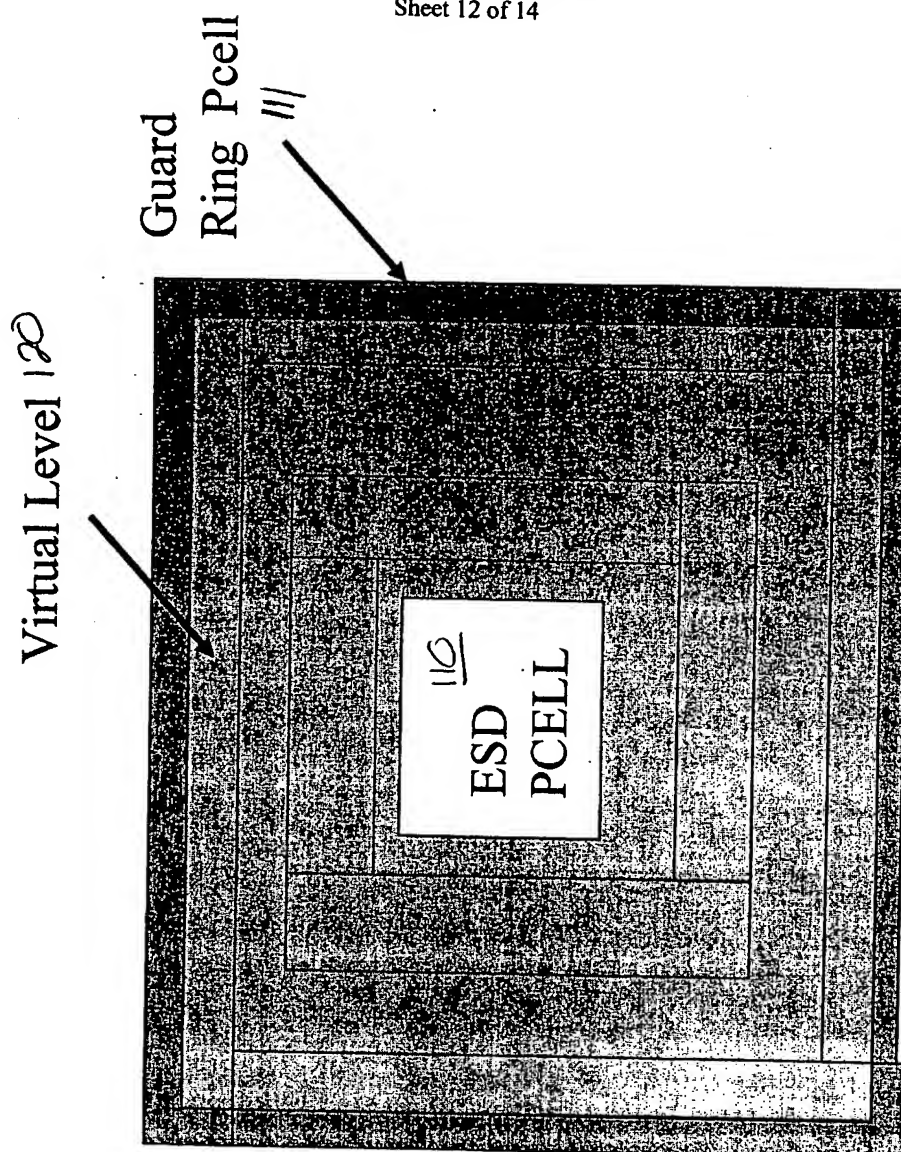


Figure 12

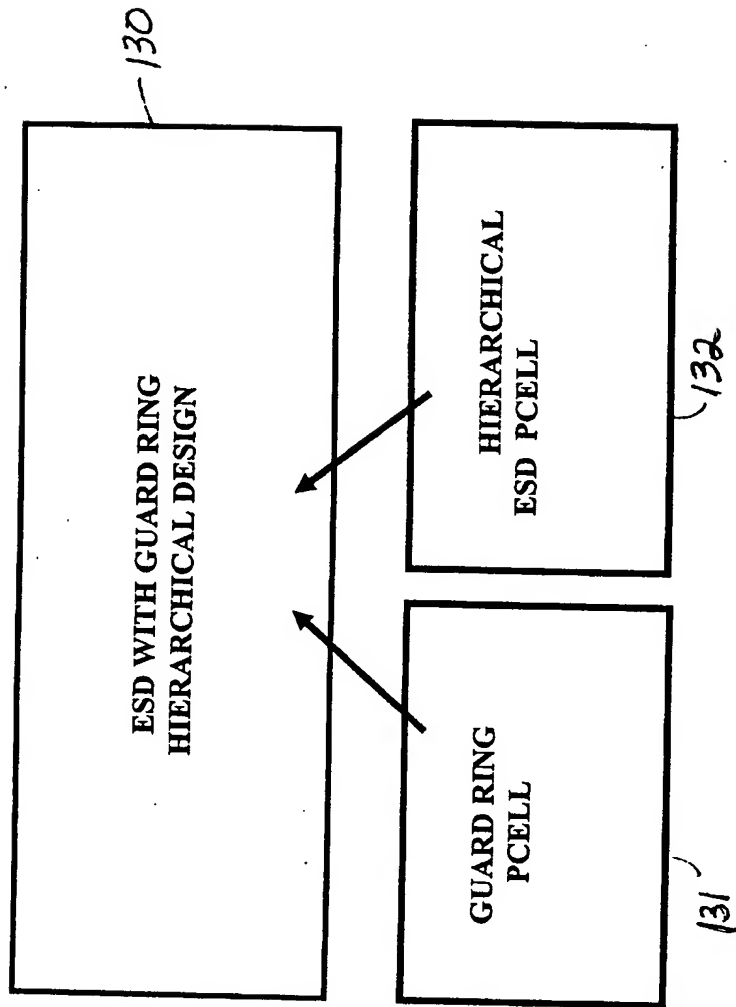


Figure 13

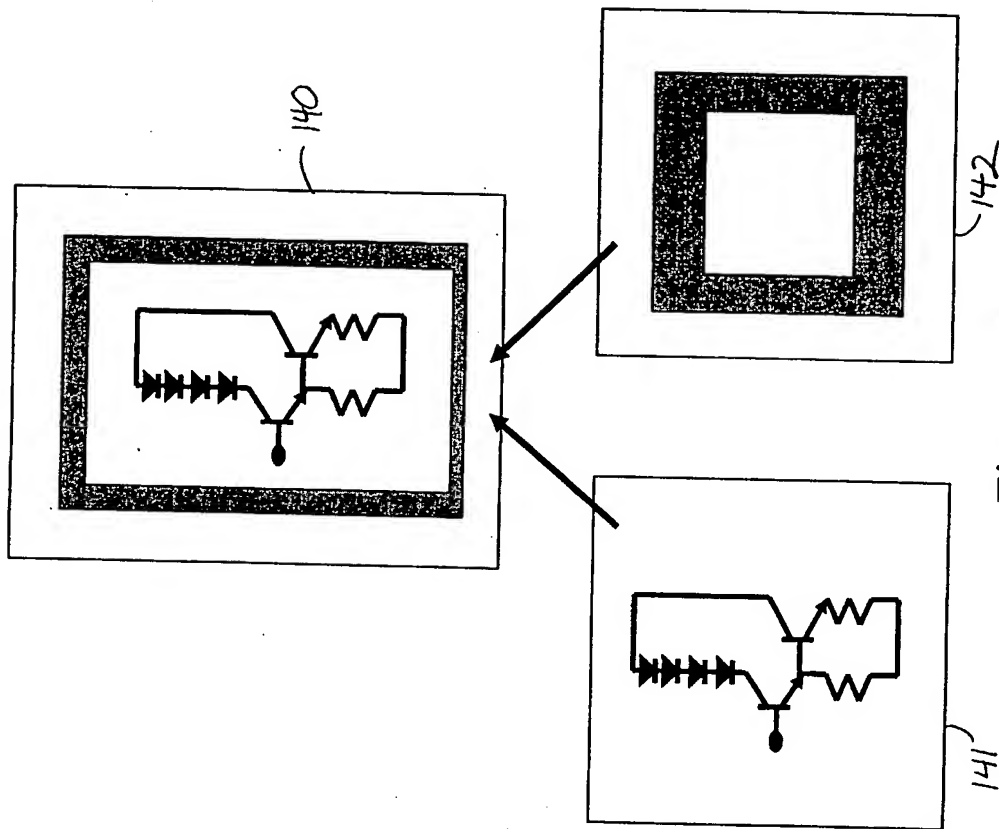


Figure 14